



Advanced Sensors and Instrumentation

Radiation Hardened Electronics Johnson Noise Thermometry

Advanced Sensors and Instrumentation (ASI) Annual Program Webinar November 4, 6-7, 2024

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RHE at ORNL (Nuclear Focus)

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- Implementation of Johnson Noise Thermometry (JNT) into Radiation Hardened Electronics (RHE) – This task focuses on JNT, which has been proposed for space reactors and SMRs as a self-calibrating thermometry system, reducing maintenance costs and increasing reliability.
- The electronics used in this system were developed at ORNL and the front-end low-noise amplifiers are JFET based making this a prime candidate for RHE development using <u>commercially available parts</u>.
- Project completion delayed to FY25 commercial parts arrived at ORNL Sept. 2024
 - M3CT-24OR0702062 Task 1: Develop RHE JNT using <u>MAGICS Technology</u> semi-custom front-end electronics and <u>RdF</u> resistance temperature device (RTD)
 - Demonstrate low-noise electronics in "clean" lab environment
 - Demonstrate low-noise electronics in "noisy" lab environment
 - Demonstrate low-noise electronics in "radiation" environment (OSURR using RTE)
 - M3CT-24OR0702061 Task 2: Integrate Notre Dame developed TEG with FREND RHE: Completed 04/2024 M3 available in PICS
- Participants: Kyle Reed; Dan Sweeney; N. Dianne Bull Ezell
 - TEG/FREND demonstration: Callie Goetz, Md Omarsany Bappy; Yanliang Zhang



Resistance and noise voltage critical for JNT measurement

What is JNT?

- JNT is the application of custom front-end electronics and signal processing to a RTD and acquire an averaged signal over time that can be used for self-calibration of proximally close RTDs from neutron degradation
- Adopted by metrology community for calibration of temperature sensors





What is JNT?

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 Why rad-hard JNT?
- Susceptible to noise injection on cable locating pre-amp and diff-amp as close to sensor as possible strengthens the application



- 1. Define the design: ORNL
- 2. (Donated) Rad-hard RTD: RdF
- 3. (Acquired) Rad-hard pre-amp with gain: MAGICS Tech





Simple Laboratory JNT System using commercial amplifiers (rad-soft)

2. Commercial 100 Ω RTD



1. & 3. Printed circuit board featuring a radiationhardened (gamma) amplifier and DAQ

Op-Amp Output -Current Source Op-Amp Output+ uint8_t transmit[] = { addr | rw, command_word}; HAL GPIO_WritePin(dev->gpio_bank, dev->cs_pin, GPIO_PIN_RESET); ADC ref HAL_SPI_TransmitReceive(dev->spi_device, transmit, receive, 8, 1); **RTD**+ HAL_GPIO_WritePin(dev->gpio_bank, dev->cs_pin, GPIO_PIN_SET); Vin (an) d magics_Init<mark>(struct</mark> magics_driver *dev, SPI_HandleTypeDef *hspi, GPIO_TypeDef *gpio_bank, uint16 dev->spi_device = hspi; Vin (dig) dev->gpio_bank = gpio_bank; RTC dev->cs pin = cs pin; ____magics_sendSPI___(dev, MAGICS_REG0, MAGICS_REG_READ, **MAGICS** Chip uint8_t receive[2]; __magics_sendSPI__(dev, MAGICS_REG29, MAGICS_REG_READ, ndef INC MAGICSMAGSEI00002NP H lefine INC MAGICSMAGSEI00002NP H uint8_t new_reg = receive[1]; new_reg |= MAGICS_REG29_EN_clock; include "stm32h7rsxx hal spi.h" magics sendSPI (dev, MAGICS REG29, MAGICS REG WRITE, efine MAGICS_STARTUP_DELAY 100 fine MAGICS_REG_READ 1 HAL_Delay(MAGICS_STARTUP_DELAY); fine MAGICS_REG_WRITE 0 Software package: efine MAGICS REG BIT 1 Acquire real-time and JNT signal MAGICS_REG0 (0x00 << MAGICS_REG_BIT)</pre> Compare two signals for drift MAGICS REG0 METAL VERSION 0x03 MAGICS REG0_DIGITAL_VERSION 0x0c fine MAGICS REGO Chip id OxfO Calibration real-time measurement from JNT signal MAGICS_REG1 (0x01 << MAGICS_REG_BIT) RH JNT MAG REV 0.0 F. Kyle Reed REG1 int new sample 0x01 Populated 10/2024 GICS REGI BIST CONT ERROR 0x04 MAGTCS REG1 BIST ONDE MAGICS REG1 int autotoggle ch1 0x10 MAGICS REG1 int autotoggle ch2 0x20

Project Overview: TEG/FREND Demonstration

- <u>Mission</u>: Harvest waste heat from nuclear applications (reactors, fuel storage, etc.) to power radiation hardened electronics (FREND) – eliminate need for external power cabling and reduce reactor penetrations
- Impactful collaboration across 2 ORNL Directorates (ESTD & FFESD) and University of Notre Dame
 - Example of National Laboratory raising the TRL of NEUP developed technology within the NEET-ASI program
- Show below, University of Notre Dame developed high power density Thermoelectric Generator (TEG) coupled with ORNL developed Front End Digitizer (FrEnD) for a successful demonstration
- Milestone completed April 2024 and available in PICS





RHE JNT:

- While JNT was originally developed for HFIR, it is applicable to advanced reactors and development continued under Space and SMR programs
- Microreactors deployed in remote locations, including space programs, will require extended operation periods with minimal-to-no maintenance for the lifetime of the reactor
- Self-calibrating instrumentation will extend the lifetime (and reduce maintenance) of critical sensors
- Radiation hardened electronics allow closer integration of electronics closer to the reactor core/sensors installation – reducing noise and increasing signal integrity (transmitting digital instead of analog signals)

Concluding Remarks

FY24 Accomplishments: (Activity Delayed)

- RHE JNT: circuit designs; PCBs fabricated; RTD (from RdF) characterized
- TEG demonstration: successfully completed TEG/FREND testing; results reported in M3-report FY25 Scope (CO, no new BA):

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- RHE JNT: Bench-top test boards in "clean" environment; "noisy" environment; and radiation environment
- Possible integration of VALTOK low-noise cable

Additional considerations for industry partners – EC/DC guidance around RHE. Guidance is available with limited distribution (CUI report: CG-MGE-1)

ORNL actively working with EC/DC reviewers on nuclear reactor technology limitations

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Thank You

